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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): NAKABAYASHI et al

Serial No.: 10/091,489

Filed: March 7, 2002

For: Fabrication Method Of Semiconductor Integrated Circuit Device

Group: 2812

Examiner:

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

April 22, 2002

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION:

Please replace the original specification with the attached Substitute Specification.

IN THE ABSTRACT OF THE DISCLOSURE:

Please replace the original abstract with the attached abstract.

TC 2 353 111 2004
APR 23 2002
RECEIVED